Claims

Having set forth the nature of the present invention, what is claimed is:

1. A system for efficiently accessing affiliated address sites, comprising:

a. an input-output subsystem for receiving affiliated address data from a network gateway;

b. a subject processor operatively connected to said input-output subsystem for initial processing of said received affiliated address data;

c. a network address sub-processor operatively connected to said subject processor for classifying processed affiliated address data; and,

d. at least one memory subsystem operatively connected to said subject processor and said network address sub-processor for holding an affiliated address control program that executes processing routines for said system.

2. A system as recited in claim 1, further comprising an array referrer operatively connected to said subject processor.

3. A system as recited in claim 1, further comprising an output device operatively connected to said input-output subsystem for communicating processed affiliated address data and system mode status information to a user.

4. A system as recited in claim 1, wherein said subject processor and said network address sub-processor comprise a single processing subsection and wherein said memory subsystem resides within said processing subsection.

5. A system as recited in claim 4, wherein memory subsystem includes means for instructing said processing subsection to find a Fourier frequency in said affiliated address input data.

6. A system as recited in claim 5, wherein memory subsystem includes means for training said processing subsection through user navigation actions.

7. A system as recited in claim 5, further including means for detecting abnormal affiliated address configurations.

8. A system as recited in claim 7, wherein said detection means comprises a fuzzy transform algorithm.

9. A system as recited in claim 2, further comprising a genetic algorithm executed by said address sub-processor for assigning node address values to data held by said array referrer.

10. A system as recited in claim 4, wherein said processing subsection is implemented in computer readable program code means.

11. A system as recited in claim 1, wherein said system is a portable wireless device in wireless communication with said gateway.

12. A system as recited in claim 2, wherein said system further comprises a display device for communicating affiliated address information to a user.

13. A system for efficiently accessing affiliated address sites, comprising:

a. means for receiving affiliated address input data from a gateway;

b. means for communicating affiliated address information processed by said system to a user;

c. means operatively connected to said receiving means for initially processing said received affiliated address data;

d. means operatively connected to said initial processing means for classifying said processed affiliated address data; and,

e. means operatively connected to said initial processing means and said classifying means for holding processing instructions for said system.

14. A system as recited in claim 13, wherein said classifying means comprises a network address sub-processor.

15. A system as recited in claim 13, wherein said receiving means comprise an input-output subsystem.

16. A system as recited in claim 13, further including means operatively connected to said initial processing means for recording historical navigation results.

17. A system as recited in claim 16, wherein said recording means comprises an array referrer.

18. A system as recited in claim 17, wherein said initial processing means executes a Fourier transform function to generate remote address locators for said input data.

19. A system as recited in claim 18, wherein said initial processing means and said classifying means are implemented in programmable firmware.

20. A system as recited in claim 13, wherein said receiving means includes means for wirelessly communicating to a network gateway.

21. A system as recited in claim 20, further including means for assigning node address values to said classifying means, and wherein node assigning means comprises a genetic algorithm executed by said classifying means.

22. A method for efficiently accessing affiliated address sites, comprising the steps of:

a. receiving affiliated address site data;

b. generating locator data associated with said received affiliated address site data;

c. analyzing said locator data and producing a set of sample values;

d. classifying said sample values in accordance with pre-established classification rules; and,

e. communicating classified affiliated address site data to a user.

23. The method as recited in claim 22, further including the step of implementing a control interface analysis before said analyzing step.

24. The method as recited in claim 23, wherein said analyzing step comprises applying a Fourier transform to said locator data.

25. The method as recited in claim 24, further including the step of normalizing said sample values after said step of applying a fast Fourier transform to said locator data.

26. The method as recited in claim 25, further including the step of scaling said sample values prior to said classification step.

27. The method as recited in claim 24, wherein said receiving step comprises receiving said data from a wireless gateway.

28. The method as recited in claim 27, wherein said step of applying a Fourier transform to said locator data comprises applying a Tukey Fourier transform.

29. A method for top level procedural user interfacing in a system for efficiently accessing affiliated address sites, comprising the steps of:

a. loading selected user options;

b. monitoring a data input device for received affiliated address data;

c. receiving said affiliated address data;

d. applying appropriate affiliated address processing functions to said affiliated address data;

e. generating a list of affiliated address sites to a user.

30. A method as recited in claim 29, further including while monitoring for received input data the steps of:

a. updating a mode status flag;

b. conducting a battery test; and,

c. updating clock and data timers.

31. A method as recited in claim 29, wherein said step of applying appropriate affiliated address processing functions to said affiliated address data includes the step of determining an applicable system operating mode.

32. A method as recited in claim 29, wherein said method is performed iteratively to provide a continuous interface to a user.